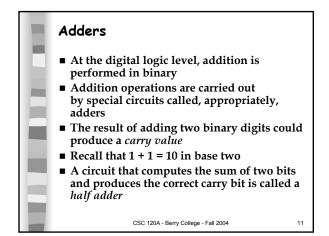


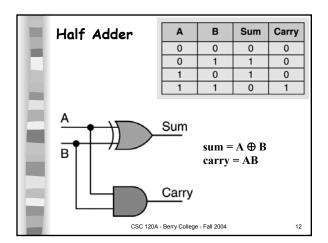
Boolean Algebra

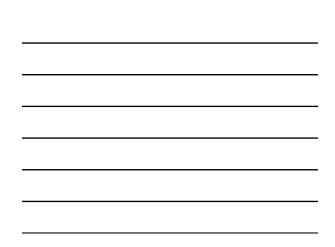
- Allows us to apply provable mathematical principles to help us design logical circuits
- Properties:

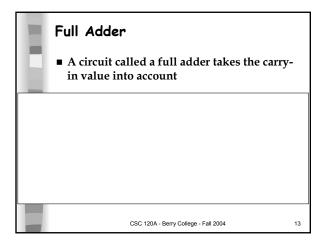
Property	AND	OR		
Commutative	AB = BA	A + B = B + A		
Associative	(AB)C = A(BC)	(A + B) + C = A + (B + C)		
Distributive	A(B + C) = (AB) + (AC)	A + (BC) = (A + B)(A + C)		
Identity	A1 = A	A + O = A		
Complement	A(A') = 0	A + (A') = 1		
DeMorgan's law	(AB)' = A' OR B'	(A + B)' = A'B'		
Demorgan's law	(AB) - A. OR B.	$[(A + B) \cdot = A \cdot B \cdot$		
	CSC 120A - Berry Colleg	ce - Fall 2004 10		

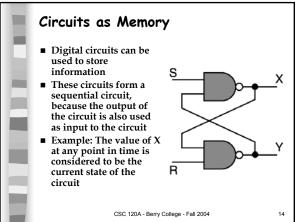


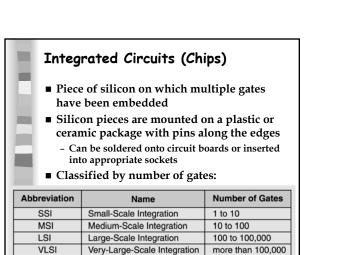






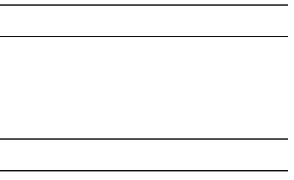




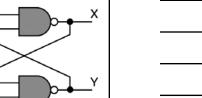


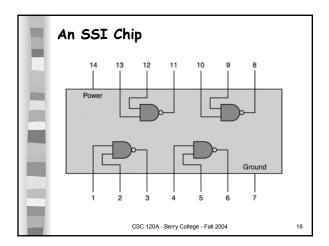
Very-Large-Scale Integration

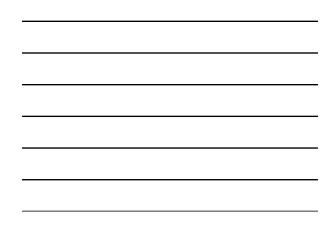
VLSI

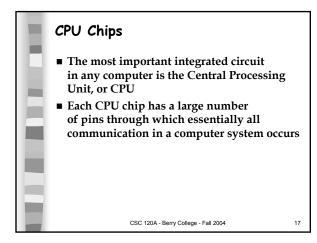


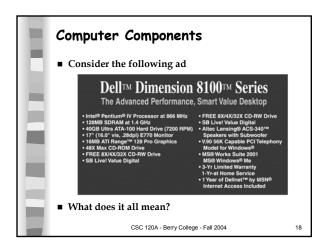














Power of 10	Power of 2	Value of Power of 2	Prefix	Abbreviation	Derivation
10-12	100		pico	р	Spanish for little
10-9			nano	n	Greek for dwarf
10-6	1200		micro	μ	Greek for small
10-3	30,0022		milli	m	Latin for thousand
10 ³	210	1024	kilo	К	Greek for thousand
106	220	1,048,576	mega	М	Greek for large
109	230	1,073,741,824	giga	G	Greek for giant
1012	240	not enough room	tera	Т	Greek for monster
1015	250	not enough room	peta	P	Greek prefix for five

